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XA-9582

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshihiro IKEDA et al.

Appln. No.: 09/987,884

Group Art Unit: 2813

Filed: November 16, 2001

Examiner: L. Schillinger

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF

* * *

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action mailed on December
30, 2003, please amend the above-identified patent
application as indicated below.

Amendments to the claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 16 of this paper.